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10/085757

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

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APPL NUM 10085757	FILING DATE 02/27/2002	CLASS A33	SUBCLASS 1.1	GAU 2842	EXAMINER JARRETT
**APPLICANTS: Chang Bo;					
**CONTINUING DATA VERIFIED: THIS APPLN CLAIMS BENEFIT OF 60/272,283 02/27/2001					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO CYPR-PM01008	
TITLE : Integrated back-end integrated circuit manufacturing assembly <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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